



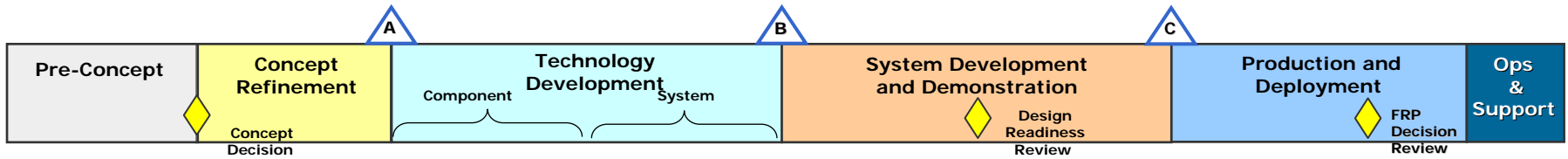
RDECOM

*How Technology Gets From
Concept to Fielding in DOD*



TECHNOLOGY DRIVEN. WARFIGHTER FOCUSED.

Michael T. Brundage
Chief, Power Applications Branch
Army Power Division
US ARMY, CERDEC
Fort Monmouth, NJ USA



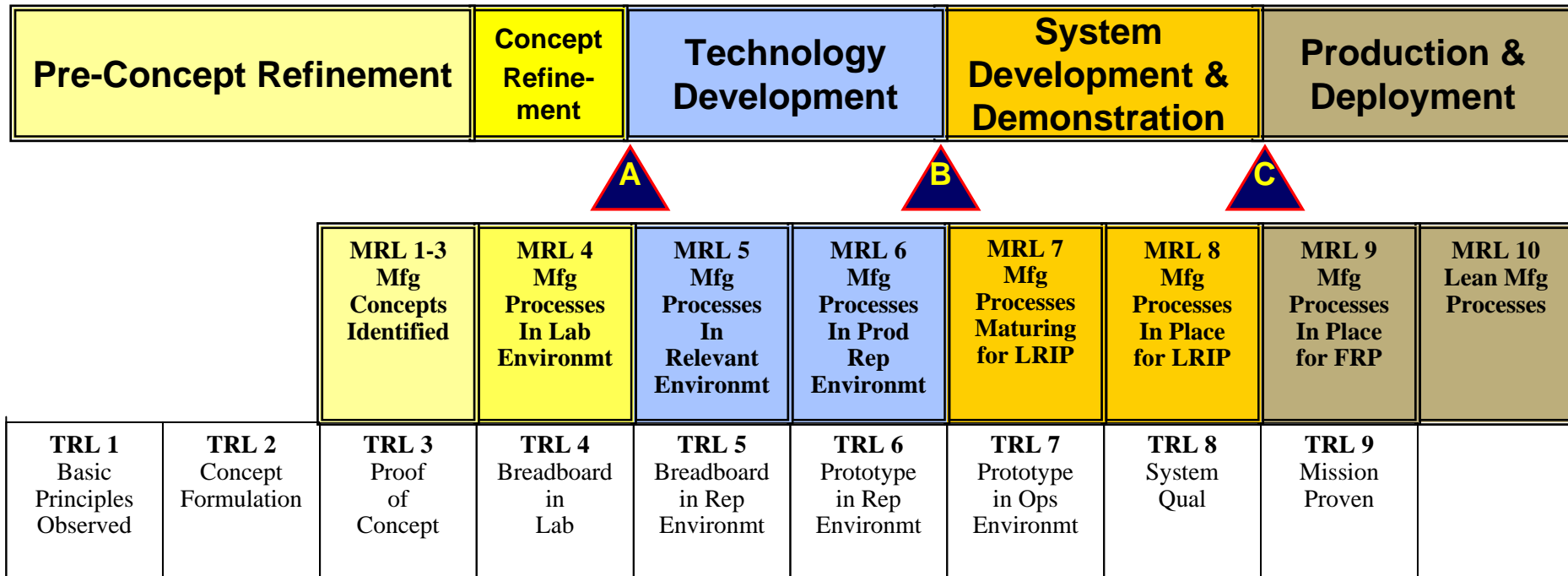
TRL 1	TRL 2	TRL 3	TRL 4	TRL 5	TRL 6	TRL 7		TRL 8	TRL 9
		MRL 3	MRL 4	MRL 5	MRL 6	MRL 7	MRL 8	MRL 9	MRL 10

TRL – Technology Readiness Level




MRL – Manufacturing Readiness Level

MRA – Manufacturing Readiness Assessment

Relationship to System Acquisition Milestones



Relationship to Technology Readiness Levels

Technology Readiness Level	Description
1. Basic principles observed and reported.	Lowest level of technology readiness. Scientific research begins to be translated into applied research and development. Examples might include paper studies of a technology's basic properties.
2. Technology concept and/or application formulated.	Invention begins. Once basic principles are observed, practical applications can be invented. Applications are speculative and there may be no proof or detailed analysis to support the assumptions. Examples are limited to analytic studies.
3. Analytical and experimental critical function and/or characteristic proof of concept.	Active research and development is initiated. This includes analytical studies and laboratory studies to physically validate analytical predictions of separate elements of the technology. Examples include components that are not yet integrated or representative.
4. Component and/or breadboard validation in laboratory environment.	 Basic technological components are integrated to establish that they will work together. This is relatively "low fidelity" compared to the eventual system. Examples include integration of "ad hoc" hardware in the laboratory.
5. Component and/or breadboard validation in relevant environment.	Fidelity of breadboard technology increases significantly. The basic technological components are integrated with reasonably realistic supporting elements so it can be tested in a simulated environment. Examples include "high fidelity" laboratory integration of components.
6. System/subsystem model or prototype demonstration in a relevant environment.	 Representative model or prototype system, which is well beyond that of TRL 5, is tested in a relevant environment. Represents a major step up in a technology's demonstrated readiness. Examples include testing a prototype in a high-fidelity laboratory environment or in simulated operational environment.
7. System prototype demonstration in an operational environment.	Prototype near, or at, planned operational system. Represents a major step up from TRL 6, requiring demonstration of an actual system prototype in an operational environment such as an aircraft, vehicle, or space. Examples include testing the prototype in a test bed aircraft.
8. Actual system completed and qualified through test and demonstration.	 Technology has been proven to work in its final form and under expected conditions. In almost all cases, this TRL represents the end of true system development. Examples include developmental test and evaluation of the system in its intended weapon system to determine if it meets design specifications.
9. Actual system proven through successful mission operations.	Actual application of the technology in its final form and under mission conditions, such as those encountered in operational test and evaluation. Examples include using the system under operational mission conditions.

<p>MRL 1-3 Mfg Concepts Identified</p>	<p>MRL 4 Mfg Processes Identified</p> <p>Key Processes Identified</p> <p>Producibility assessment initiated</p>	<p>MRL 5 Mfg Processes Develop.</p> <p>Mfg equipment in relative environment</p> <p>Producibility assessment ongoing</p> <p>Cost drivers identified</p>	<p>MRL 6 Critical Mfg Processes Demo'd</p> <p>Mfg equipment in relevant environment</p> <p>Producibility assessment ongoing</p> <p>Cost drivers analyzed</p> <p>Long lead items identified</p>	<p>MRL 7 Prototype Mfg System</p> <p>Mfg processes in validation</p> <p>Producibility improvement underway</p> <p>Trade studies conducted</p> <p>Supply chain validated</p> <p>Long lead plans in place</p>	<p>MRL 8 Process Maturity Demo</p> <p>All materials ready for LRIP</p> <p>Mfg processes proven for LRIP</p> <p>Supply chain established</p>	<p>MRL 9 Mfg Processes Proven</p> <p>Overall Mfg Process Operates At target Quality, Cost and Lead times</p> <p>All key Processes Meet process Control Targets</p>	<p>MRL 10 Highest Production Readiness</p> <p>System in Production Or Meets Engineering Performance & Reliability</p> <p>Overall Mfg Process Operates At 6-Sigma Quality, and Meets Cost and Lead times Estimates</p>
	A			B		C	

Bridging R&D through to Production

TIMELINE

Concept

Basic R&D

ARL, DARPA,
Universities,
Academia,
Contractors

RDEC RDT&E

Advanced R&D
Prototype Engineering
Testing
Specification
Preparation
System Integration
Tech Transition to
PEO/PM's, Major
Weapons Systems,
Programs of Record

Production

ARP Preparation (Specs, SOW's, Sections
L&M Criteria), SSEB, SSAC, QALI, IPR's,
IGT, SAR, COTR, ECP's

DLA/Acq Ctr/Item Mgr

Solicitation Preparation, SSEB, SSAC,
Source Selection(s), Contract Award(s),
Contract Administration & Monitoring, Item
Management, Requisition Processing

